

**Women & Minorities in Engineering Program Undergraduate Research Program  
SRC Undergraduate Scholar Internship Application**

**PERSONAL INFORMATION**

**Name (First & Last) :** \_\_\_\_\_ **OSU ID :** \_\_\_\_\_

**Street :** \_\_\_\_\_ **City :** \_\_\_\_\_ **State :** \_\_\_\_\_ **Zip Code :** \_\_\_\_\_

**Phone # :** \_\_\_\_\_ **E-Mail :** \_\_\_\_\_ **Gender :** \_\_\_\_\_

**Are you a US Citizen or Permanent Resident?**

**Ethnic Background :**     African American     Asian American / Pacific Islander     Latina / Hispanic

Caucasian     Native American / Alaskan Native     Other : \_\_\_\_\_

**ACADEMIC INFORMATION**

**Major :** \_\_\_\_\_ **Year in College (Fall) :** \_\_\_\_\_ **Graduation Date :** \_\_\_\_\_

**OSU Cumulative GPA :** \_\_\_\_\_ **Last Term's GPA :** \_\_\_\_\_

**Have you previously participated in an Undergraduate Research Program at OSU?**

**FACULTY SPONSOR INFORMATION**

In order to qualify for the Undergraduate Research internship, Undergraduate students may work from 5-15 hours per week during fall, winter, and/or spring term(s). Research must be in the semi-conductor and information technology fields. Specific research topics are listed below.

**Professor Name (First & Last) :** \_\_\_\_\_ **E-Mail :** \_\_\_\_\_

**Campus Address :** \_\_\_\_\_ **Intel Research Topic Category (See List Below) :** \_\_\_\_\_

*By signing this document, I agree to sponsor the undergraduate student listed above.*    **Professor Signature :** \_\_\_\_\_

- |   |  |
|---|--|
| (1) Analog, digital or RF design                                  | (12) Semiconductor package design and test |
| (2) Micro architecture techniques (multi-core and multi-thread)   | (13) VLSI-CMOS and semiconductor physics   |
| (3) System Architecture (Hardware and Software)                   | (14) High Speed signal processing          |
| (4) Human Computer Interface (Speech, Handwriting, Audio, Vision) | (15) High Speed low power design issues    |
| (5) Wireless communication and networking                         | (16) Process and yield enhancement         |
| (6) Visualization techniques                                      | (17) Semiconductor tool design             |
| (7) Compilers and run-time systems                                | (18) Mixed signal logic and circuit design |
| (8) Information and knowledge representation                      | (19) Lithography and dry etch research     |
| (9) Distributed and pervasive computing                           | (20) Advanced thin films research          |
| (10) Pervasive computing  | (21) Optical modeling                      |
| (11) Electronic design automation and CAD Tools                   | (22) Multi and Many core programming       |

**Completed applications should be sent to:**

Women and Minorities in Engineering  
147 Batcheller Hall Corvallis OR 97331-2409  
phone: 541-737-9699  
email: Ellen.Momsen@oregonstate.edu

**A completed application includes:**

- Application Form  
- Personal statement explaining why you are interested in participating in research as an undergraduate student.  
Last revised: 12/12/12